IN THE CLAIMS

Please amend the claims as follows:

Claims 1-4 (Canceled).

Claim 5 (Currently Amended): An [[IPS]] In Plane Switching (IPS) liquid crystal displaying apparatus, comprising:

a [[TFT]] Thin Film Transistor (TFT) array substrate;

an opposite substrate opposed to said TFT array substrate; and

liquid crystal interposed between said TFT array substrate and said opposite substrate, wherein said TFT array substrate comprises,

a glass substrate,

a gate insulating film formed on said glass substrate,

a passivation film formed on said gate insulating film,

a plurality of scanning lines configured to transmit ~~for transmitting~~ a plurality of scanning signals ~~signal, said plurality of scanning lines being~~ and formed on said glass substrate,

a plurality of signal lines configured to transmit ~~for transmitting an~~ a plurality of image signals ~~signal, said plurality of signal lines being~~ and formed on said gate insulating film,

a plurality of pixels arranged in a grid like pattern by crossing said plurality of scanning lines with said plurality of signal lines,

a plurality of TFTs implementing a switching operation of said plurality of image signals ~~signal~~ based on said plurality of scanning signals,

a plurality of driving electrodes formed on said passivation film and connected with said plurality of TFTs,

a plurality of opposite electrodes formed on said passivation film, each of said plurality of opposite electrodes opposing ~~configured to be opposed to~~ each of said plurality of driving electrodes, and

a plurality of common lines configured to connect ~~for connecting~~ each of said plurality of opposite electrodes ~~of one~~ with each of said plurality of pixels ~~with another one of said plurality of pixels~~,

wherein ~~said TFT array substrate is formed on said passivation film, said passivation film being different from a layer provided with said driving electrode and said opposite electrode, and~~ said TFT array substrate includes [[a]] at least one common line of said plurality of common lines and [[a]] at least one scanning line of said plurality of scanning lines on a same layer, and [[a]] at least one signal line of said plurality of signal lines provided on said gate insulating film.

Claim 6 (Previously Presented): The IPS liquid crystal displaying apparatus of Claim 5, wherein said TFT array substrate is provided with a passivation film, a surface of which is approximately flat in shape.

Claim 7 (Currently Amended): The IPS liquid crystal displaying apparatus of Claim 5, wherein said TFT array substrate is provided with a light shielding means formed in such a manner as to superpose one signal line of the plurality of signal lines and one ~~that said signal line is superposed with said~~ opposite electrode of the plurality of opposite electrodes.

Claim 8 (Currently Amended): The IPS liquid crystal displaying apparatus of Claim 5, wherein said TFT array substrate ~~is formed such that a~~ further comprises:

at least one TFT of said plurality of TFTs; ~~for switching said image signal in accordance with said scanning signal, a~~

at least one driving electrode of said plurality of driving electrodes ~~for accumulating while switch of said TFT is off electric load stored when said switch of said TFT is on,~~; and

at least one [[a]] storage capacity increasing electrode ~~for reinforceing the accumulationg force of said a driving electrode are respectively superposed~~,

wherein said at least one TFT of said plurality of TFTs, said at least one driving electrode of said plurality of driving electrodes, and said at least one storage capacity increasing electrode are formed in different layers of said TFT array substrate ~~layer~~.